The Center for Engineering Learning & Teaching (CELT) was established in 1998 in the College of Engineering at the University of Washington. We are the first center in the nation in a College of Engineering to combine a research and instructional development mission. Over the past decade, we have been conducting in depth research on understanding how engineering education and years of experience shape individuals’ engineering design processes.

**Studieds of the Engineering design Process**

**Method**

Participants: Data were collected from 32 freshmen, 32 senior engineering students, and 19 practicing engineers (experts).

Experiment: Participants were given 3 hours to design a community playground (working individually in a lab setting) and asked to think aloud. These sessions were audio recorded.

**Analyses**

Coding: To understand the design process, we synthesized a prescriptive model of how design is accomplished from several engineering design texts, as detailed in the table below. The design activity definitions were used to code the transcripts of the verbal protocol data. Researchers also conducted a separate quality scoring of each participant’s final playground design.

<table>
<thead>
<tr>
<th>Design Stages</th>
<th>Activities Involved</th>
</tr>
</thead>
<tbody>
<tr>
<td>Problem Scoping (PS)</td>
<td>Problem Definition (PD), Gathering Information (GATH)</td>
</tr>
<tr>
<td>Designing Alternative Solutions (DAS)</td>
<td>Generating Ideas (GEN), Modeling (MOD), Feasibility Analysis (FEAS), Evaluation (EVAL)</td>
</tr>
<tr>
<td>Project Realization (PR)</td>
<td>Decision (DEC), Communication (COM)</td>
</tr>
</tbody>
</table>

**Selected findings**

Compared to freshmen, seniors have higher quality designs and scope the problem more effectively by considering more categories of information. Seniors also make more transitions among design steps and progress further in the design process.

Compared to students, experts spend more time in all design stages and gather more information (explicitly) that covers more information categories. Experts also tend to exhibit a “cascade” pattern of transitions.

**Graphical Representations**

We have developed and refined three methods for graphically representing the design process over time. These representations utilize the same timestamped, segmented transcripts as data.

**Process Timelines**

For each design stage/activity, a separate timeline indicates when and for how long the participant engaged in that stage/activity during the overall design session.

**Cumulative Time Plot**

A running total of the time spent in each stage/activity is plotted across the overall design session. Provides for clear quantitative comparisons between the different stages/activities.

**Progress Time Plot**

Separate curves for each stage/activity indicate fraction of total time eventually spent in that stage/activity. Makes clear the shifting emphases the participant places on stages/activities throughout the design session.
COMPARING SENIOR ENGINEERING STUDENTS WITH DIFFERING QUALITY OF DESIGNS

REFERENCES

More CELT publications can be found at http://depts.washington.edu/celtweb/